

In the Claims

Claims 1-70 (cancelled).

Claim 71 (original): A computer system comprising:

a signal source arranged to provide a data signal; and

an inverter coupled with the signal source, configured to invert the data signal and arranged to output the inverted signal; the inverter including:

a crystalline layer comprising silicon and germanium;

a first transistor device supported by the crystalline layer, the first transistor device comprising a first gate and a first active region proximate the first gate; the first active region including a first channel region and a pair of first source/drain regions; at least a portion of the first active region being within the crystalline layer; an entirety of the first active region within the crystalline layer being within a single crystal of the crystalline layer;

a second transistor device, the second transistor device comprising a second gate and a pair of second source/drain regions;

the first and second gates being electrically connected to one another, and being in electrical connection with the signal source; and

one of the first source/drain regions being electrically connected with one of the second source/drain regions and being in electrical connection with the output.

Claim 72 (original): The computer system of claim 71 wherein the crystalline layer has a relaxed crystalline lattice, and further comprising a strained crystalline lattice layer between the crystalline layer and the first transistor device gate.

Claim 73 (original): The computer system of claim 72 wherein the strained crystalline lattice layer includes silicon.

Claim 74 (original): The computer system of claim 73 wherein the first transistor device is an NFET device.

Claim 75 (original): The computer system of claim 73 wherein the first transistor device is a PFET device.

Claim 76 (original): The computer system of claim 72 wherein the strained crystalline lattice layer includes silicon and germanium.

Claim 77 (original): The computer system of claim 76 wherein the transistor device is a PFET device.

Claim 78 (original): The computer system of claim 72 wherein the entirety of the relaxed crystalline lattice is a single crystal.

Claim 79 (original): The computer system of claim 72 wherein the relaxed crystalline lattice is polycrystalline.

Claim 80 (original): The computer system of claim 72 wherein the relaxed crystalline lattice includes Si/Ge.

Claim 81 (original): The computer system of claim 80 wherein the relaxed crystalline lattice comprises from about 10 to about 60 atomic percent germanium.

Claims 82-88 (canceled).